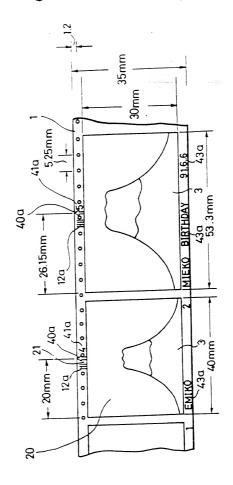
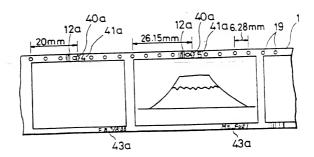
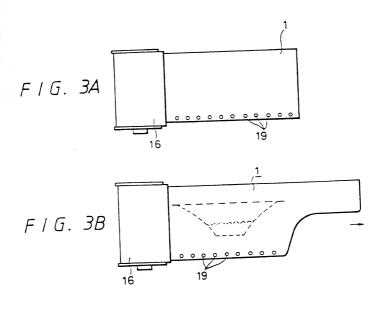
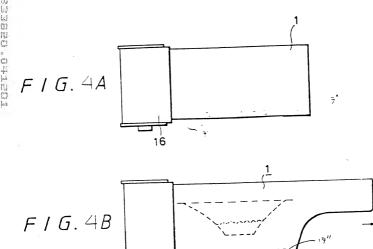
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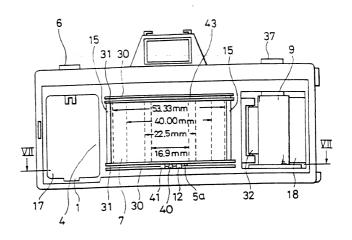




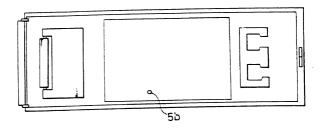




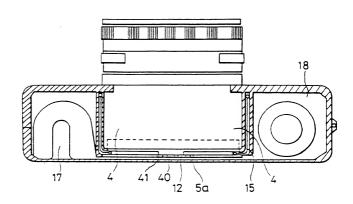
16



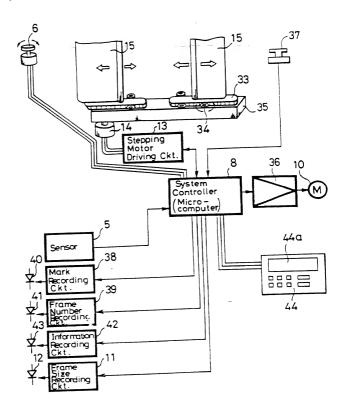
F1G.6

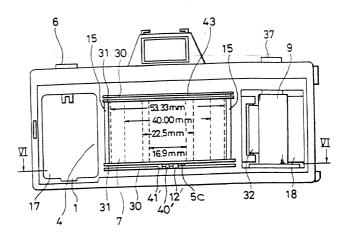


F1G. 7

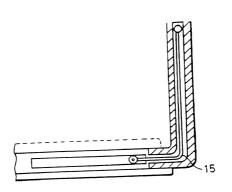


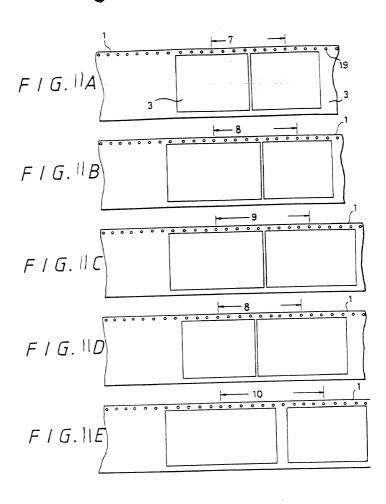
r 1 U. 🛎



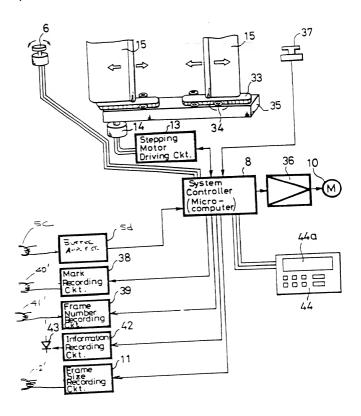


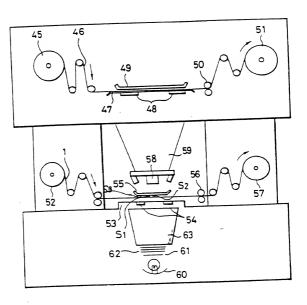
F1G. 10





F1U. 14

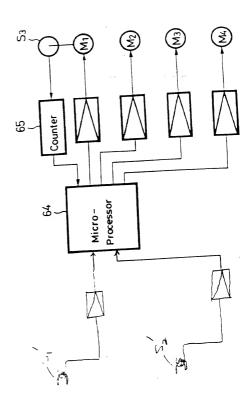


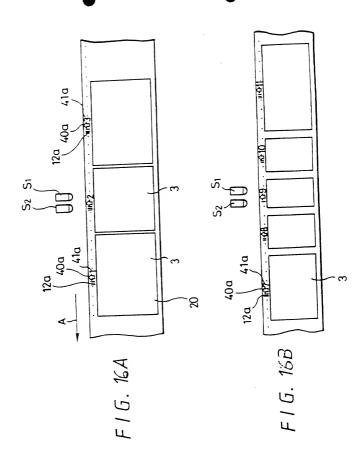


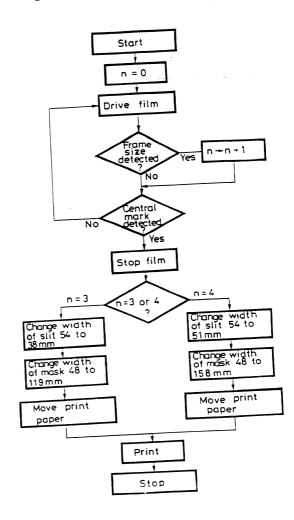
Counter Processor 49 Micro-5 Š, ₹ CC - Vcc

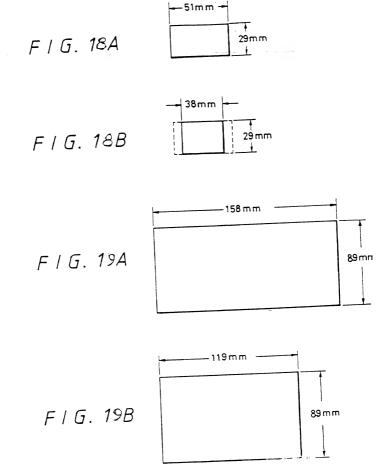
F16.14

F16.15

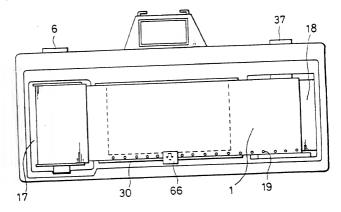




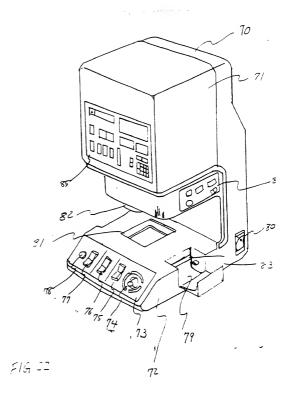




F 1 G. 21

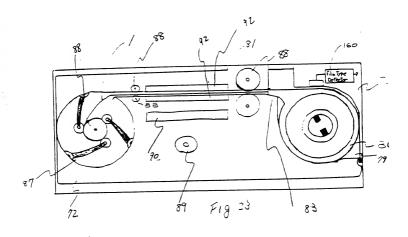


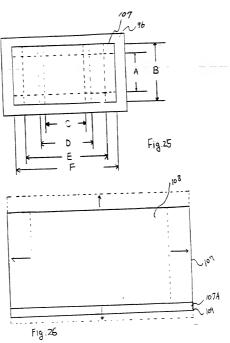
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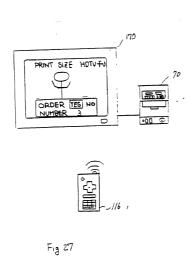


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100







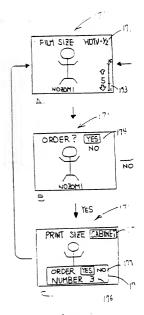
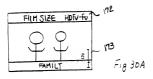


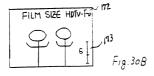
Fig 28

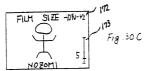
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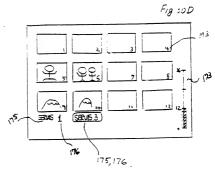
Image Controller/ Superimpose CK:1

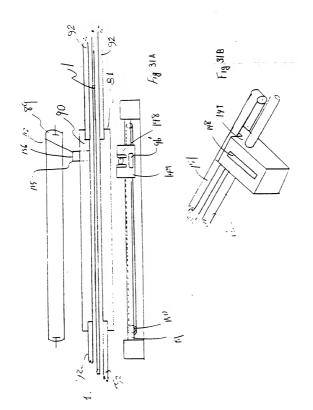
Memory











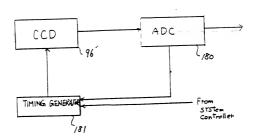


Fig.32